(08 Marks)

(08 Marks)

(04 Marks)

(10 Marks)

(10 Marks)

Fourth Semester B.E. Degree Examination, June/July 2019 **Microprocessors**

Time: 3 hrs.

Max. Marks: 100

Note: Answer any FIVE full questions, selecting atleast TWO questions from each part.

| | | aneast 1110 questions from even from | |
|---|----|--|-------------|
| | | PART – A | |
| | | With a neat functional block diagram discuss the architecture of 8086 processor. | (10 Marks) |
| 1 | a. | With a neat functional block diagram discuss the architecture of each partial and a second pa | (06 Marks) |
| | b. | Describe the process of physical address generation in 8086 with an example. | (04 Marks) |
| | c. | Compare and contrast real and protected mode memory addressing. | |
| | | the maring mechanism of advanced processors. | (10 Marks) |
| 2 | a. | Define Paging. Draw and discuss the paging mechanism of advanced processors. | |
| | b. | Define addressing mode. Discuss any five data addressing modes with an examp | (06 Marks) |
| | | 1. Can the following instructions: | |
| | C. | Construct machine code for the following instructions: (i) MOV BX, CX (ii) MOV DL, [SI + 1240H] | (04 Marks) |
| | | (i) MOV BX, CX (ii) MOV DL, [SI + 1240H] | |
| | | A visit of the or avample: | |
| 3 | a. | Explain the following instructions with an example: (i) MUI (ii) XLAT (iii) AAM (iv) CMPSB (iv) LOCK | (10 Marks) |
| | | (i) MUL (ii) XLAT (iii) AAM (iv) CMPSB (iv) LOCK | |
| | b. | (i) MUL (ii) ALA1 (iii) AAM Define assembler directives. Explain the following assembler directives with an e (i) PUBLIC (ii) ORG (iii) PROC (iv) EQU | (10 Marks) |
| | | (i) PUBLIC (ii) ORG (iii) PROC (iv) EQU | |
| | | | (08 Marks) |
| 4 | a. | Discuss shift instructions with an example. | |
| | b. | Discuss shift instructions with an example. Write an assembly level program to sort an array of N bytes in an ascending | (08 Marks) |
| | | hubble cort technique | (04 Marks) |
| | c. | What are the differences between NEAR and FAR procedures. | |
| | | | |
| | | PART - B | |
| - | | Compare MACRO with procedure. | (04 Marks) |
| 5 | a. | The state of the s | ASCII. |
| | b. | Definite iviodular programmang. | (08 Marks) |
| | c. | Develop an assembly level program to read 2 digit Hexadecimal number and | display the |
| | C. | same on the screen. | (08 Marks) |
| | 0 | | |
| 6 | 0 | What are the functions of the following pins of 8086 processor: | |
| U | a. | | (10 Marks) |
| | | (1) ALL (11) Will was the maximum mode of 8086 processors. | (10 Marks) |
| | b | Draw and discuss the maximum mode of cook partial | |

Any revealing of identification, appeal to evaluator and /or equations written eg, 42+8 = 50, will be treated as malpractice. Important Note: 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages.

a.

b.

C.

a.

7

8

* * * * *

Explain 8086 memory interfacing with an example.

What is IVT? Explain IVT with its structure.

Explain absolute address decoding technique with its block diagram.

Write differences between I/O mapped I/O and memory mapped I/O.

Describe the architecture of 8255 PPI, with its block diagram.